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# TCG Mobile Reference Architecture

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Version	2
Revision	45
March 6, 2023	

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**Work in Progress**

PUBLIC REVIEW

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## CHANGE HISTORY

REVISION	DATE	DESCRIPTION
2/1	August 23, 2018	<ul style="list-style-type: none"> <li>Initial Release of Version 1.00.</li> </ul>
2/2	October 2, 2018	<ul style="list-style-type: none"> <li>Updated, more granular comments</li> </ul>
2/3	October 30, 2018	<ul style="list-style-type: none"> <li>Added comments from 10/30 MPWG Review</li> </ul>
2/4	November 13, 2018	<ul style="list-style-type: none"> <li>Added comments from 11/13 MPWG Review</li> </ul>
2/5	November 27, 2018	<ul style="list-style-type: none"> <li>Added comments from 11/27 MPWG Review</li> </ul>
2/6	June 5, 2019	<ul style="list-style-type: none"> <li>Implemented a number of minor fixes that we noted (and accepted changes on the mundane ones for clarity), thorough discussion of boot sequence</li> <li>Began moving informative sections to the Annex</li> </ul>
2/7	June 5, 2019	<ul style="list-style-type: none"> <li>Added comment from 6/24 MPWG Review</li> <li>Restored background sections in the body of the document, global update of TPM to TPM Module, moved Architecture section earlier, and updates to the Mobile Device Architecture and Boot Sequence sections</li> </ul>
2/8	August 31, 2019	<ul style="list-style-type: none"> <li>Added comments from 7/15/19 MPWG Review</li> <li>Accepted some changes</li> <li>Moved some DICE/MARS references up to the scope section</li> <li>Started drafting Architecture section; included TCG technologies</li> </ul>
2/9	November 25, 2019	<ul style="list-style-type: none"> <li>Added comments from 9/16/19 MPWG Review</li> <li>Removed MARS reference (but keep tracking that change if there's a publication to reference in the future)</li> <li>More work on the trusted computing architecture section, pulling in example graphics and more work on the language</li> </ul>
2/10	January 7, 2020	<ul style="list-style-type: none"> <li>Added comments from 11/25/19 MPWG Review</li> <li>Accepted some changes</li> <li>Update footer date</li> <li>Updates to the architecture section to paraphrase and reference other TCG specs</li> <li>Update to the architecture section graphics</li> </ul>
2/11	March 1, 2020	<ul style="list-style-type: none"> <li>Added comments from 1/27/2020</li> <li>Revised the TCG part of the Trusted Computing Architecture section again.</li> <li>Removed TSS figure and referenced it in the TSS doc. Same with TNC and SED.</li> <li>Began describing relationship to other orgs specifications</li> </ul>
2/12	April 18, 2020	<ul style="list-style-type: none"> <li>Cleaning up the unfinished architecture section, adding comments for future clarity and moving to another section for now.</li> <li>Minor updates to the Device Boot Section and comments for review</li> <li>Comments for review to a Protected Environment Section</li> </ul>
2/13	May 16, 2020	<ul style="list-style-type: none"> <li>Updates based on 4/27/2020 MPWG review</li> <li>Made Secure Boot section normative and updated language accordingly</li> <li>Added some figures from ARM documentation for reference only</li> <li>Removed SBPS Section and any references to it</li> <li>Removed TPM Identity &amp; Ownership Section</li> </ul>
2/14	June 14	<ul style="list-style-type: none"> <li>Updates based on 6/X/20 MPWG review</li> <li>Accepted many changes for clarity.</li> <li>Update 1.4 Document Structure, and Section 3</li> <li>Remove 1 example from Section 4</li> </ul>
2/15	July 18, 2020	<ul style="list-style-type: none"> <li>Updates based on 6/17 MM Review</li> <li>Removed graphics in the Annexes</li> <li>Re-organized sections</li> <li>Make the introduction less TPM-centric</li> <li>Review and comment RoT section (in its new place)</li> </ul>

2/16	August 13, 2020	<ul style="list-style-type: none"> <li>• Updates based on 7/20/20 MM Review</li> <li>• Accept changes</li> <li>• Make Scope less TPM-specific</li> <li>• Begin re-write of RoT section</li> </ul>
2/17	October 4, 2020	<ul style="list-style-type: none"> <li>• Accepted changes</li> <li>• Revised draft of RoT section and removed RoT discussion from appendix</li> <li>• Updates to the Trusted Computing Architecture section</li> </ul>
2/18	October 26, 2020	<ul style="list-style-type: none"> <li>• Moved several sections to the Appendix</li> <li>• Accepted a lot of changes</li> <li>• Moved unused terms out of the Glossary</li> <li>• Added sentences to the RoT</li> <li>• Comments from 10/26 review</li> </ul>
2/19	December 29, 2020	<ul style="list-style-type: none"> <li>• Accepted changes (esp in RoT section)</li> <li>• Updates to PE section</li> <li>• Notes on classification of PE reqs and section structure for discussion</li> <li>• Modifications to Appendix structure and content</li> </ul>
2/20	January 28, 2021	<ul style="list-style-type: none"> <li>• Updates to the Mobile Platform Boot Sequence section and new proposed outline.</li> </ul>
2/21	March 18, 2021	<ul style="list-style-type: none"> <li>• Boot Sequence section updates. <ul style="list-style-type: none"> <li>◦ Moving normative sentence to PE section</li> <li>◦ Moved section to the Appendix</li> <li>◦ Drafted new text for secure/measured boot technologies</li> <li>◦ Accepted changes to clean it up</li> </ul> </li> </ul>
2/22	March 27, 2021	<ul style="list-style-type: none"> <li>• Protected Environment updates <ul style="list-style-type: none"> <li>◦ Revamped Introductory text</li> <li>◦ Reorganized requirements</li> <li>◦ Accepted changes to clean it up</li> </ul> </li> </ul>
2/23	April 11, 2021	<ul style="list-style-type: none"> <li>• Protected Environment updates <ul style="list-style-type: none"> <li>◦ Continued re-organization (heading, moving a few around)</li> <li>◦ Revise and cut some requirements</li> <li>◦ Finish first pass</li> </ul> </li> <li>• Change from device to mobile platform</li> </ul>
2/24	April 18, 2021	<ul style="list-style-type: none"> <li>• Replace trusted computing standards appendix with API appendix <ul style="list-style-type: none"> <li>◦ Fix document structure</li> </ul> </li> <li>• Draft PE communication/API requirements</li> <li>• Added PE CIA Table</li> </ul>
2/25	May 5, 2021	<ul style="list-style-type: none"> <li>• Re-write of Trusted Application Implementation section</li> </ul>
2/26	May 17, 2021	<ul style="list-style-type: none"> <li>• Re-write of Annex C Trusted Computing Interfaces.</li> <li>• Re-write of Annex D PE Implementation Examples</li> <li>• Cleaning up some References</li> </ul>
2/27	June 12, 2021	<ul style="list-style-type: none"> <li>• Update key word language</li> <li>• Addressing minor comments throughout</li> <li>• Fix cross-references</li> <li>• Update references</li> </ul>
2/28	August 19, 2021	<ul style="list-style-type: none"> <li>• Updating references in Glossary</li> <li>• Adding comments/updates from a beginning to end read-through</li> <li>• Update Roots of Trusts and references</li> <li>• Add trusted computing APIs</li> </ul>
2/29	August 27, 2021	<ul style="list-style-type: none"> <li>• Accepted a lot of changes</li> <li>• Added acknowledgments</li> <li>• Updated RoT definitions and references</li> <li>• Updated Trusted Computing Interfaces</li> <li>• Updated Requirements references</li> </ul>
2/30	October 8, 2021	<ul style="list-style-type: none"> <li>• Updates from end-to-end read through of this and MCP</li> </ul>
2/31	November 13, 2021	<ul style="list-style-type: none"> <li>• Clean up and acknowledgements</li> </ul>
2/32	December 10, 2021	<ul style="list-style-type: none"> <li>• Correct document version (change from 3 to 2), and accepted changes.</li> </ul>
2/33	December 27, 2021	<ul style="list-style-type: none"> <li>• Merge versions with recent comments and document version update</li> </ul>

2/34	December 29, 2021	<ul style="list-style-type: none"> <li>Addressing recent straightforward comments (not the “more significant” comments)</li> </ul>
2/35	March 13, 2022	<ul style="list-style-type: none"> <li>Addressing several “more significant” in acknowledgements, Roots of Trust, and PE Requirements sections</li> </ul>
2/36	April 4, 2022	<ul style="list-style-type: none"> <li>Revisions to Protected Environment Requirements based on MPWG review</li> <li>Change Mobile Platform Architectures to Mobile Platform Structures</li> <li>Re-organize Mobile Platform Boot Phases Appendix</li> </ul>
2/37	August 21, 2022	<ul style="list-style-type: none"> <li>Finished revisions to RoT Section.</li> <li>Drafted Chains of Trust text</li> <li>Revised Boot Sequence section</li> <li>Protected Environment Requirements Updates in progress</li> </ul>
2/38	September 11, 2022	<ul style="list-style-type: none"> <li>Accepting changes and cleaning up document</li> <li>Adding some informative labels</li> <li>Adding some references to RIP, NIST Supply Chain, etc</li> <li>Re-ordering references</li> </ul>
2/39	September 18, 2022	<ul style="list-style-type: none"> <li>Very minor tweaks caught during final review.</li> </ul>
2/40	September 18, 2022	Accepted all changes in document.
2/41	November 2, 2022	<ul style="list-style-type: none"> <li>Addressing comments from pre-tech committee review.</li> <li>Significant: Added Mobile Platform Requirements Section before Protected Environment Requirements Section and moved several requirements into it.</li> <li>Included cross-references between Annex A and D</li> <li>Accepted many changes for clarity.</li> </ul>
2/42	November 15, 2022	<ul style="list-style-type: none"> <li>Minor tweaks from MPWG 11/14 Review</li> </ul>
2/43	November 16, 2022	<ul style="list-style-type: none"> <li>Accepted all changes in document.</li> </ul>
2/44	February 20, 2023	<ul style="list-style-type: none"> <li>Addressing all Technical Committee Comments</li> </ul>
2/45	March 6, 2023	<ul style="list-style-type: none"> <li>Accepted all changes in document.</li> </ul>

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## ACKNOWLEDGEMENTS

The TCG wishes to thank all those who contributed to this specification. This document builds on work done over the course of 10 years in various TCG work groups. Special thanks to the following current and former members of the TCG who contributed to this document:

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# CONTENTS

DISCLAIMERS, NOTICES, AND LICENSE TERMS	1
CHANGE HISTORY	2
1 SCOPE	8
1.1 Key Words	8
1.2 Statement Type	8
1.3 References	8
1.4 Document Structure	11
2 Glossary	12
3 Introduction	14
4 Roots of Trust	15
4.1 Chains of Trust	15
5 Mobile Platform Boot Sequences	17
5.1 Boot ROM	17
5.2 Secure Boot	17
5.3 Measured Boot	17
5.4 Overlapping Secure Boot and Measured Boot	17
6 Mobile Platform Requirements	18
6.1 Mobile Platform Fundamental Requirements	18
6.2 Mobile Platform Secure Boot Requirements	18
6.3 Mobile Platform Measured Boot Requirements	18
6.4 Mobile Platform Update Requirements	19
6.5 Mobile Platform Runtime Requirements	19
6.6 Mobile Platform Storage Requirements	19
7 Protected Environment Requirements	20
7.1 Protected Environment Fundamental Requirements	20
7.2 Protected Environment Secure Boot Requirements	20
7.3 Protected Environment Update Requirements	20
7.4 Protected Environment Runtime Requirements	20
7.5 Protected Environment Storage Requirements	21
8 Trusted Application Requirements	22
8.1 Trusted Application Constraints	22
A. Mobile Platform Structure	23
A.1 Protected Environment Isolated by Processor Features	23
A.2 Protected Environment Isolated by a Hypervisor	23
A.3 Protected Environment Isolated by Processors or Cores	24

DRAFT

B.	Mobile Platform Boot Sequence Examples.....	26
B.1	Secure Boot Examples.....	26
B.1.1	Unified Extensible Firmware Interface (UEFI) Secure Boot .....	26
B.1.2	U-Boot.....	26
B.2	Measured Boot Examples .....	26
B.2.1	TPM Mobile.....	26
B.2.2	Measurement and Attestation Roots (MARS) .....	26
B.2.3	Device Identifier Composition Engine (DICE) .....	26
C.	Trusted Computing Communication Interfaces.....	28
D.	Protected Environment Implementation Examples .....	29
D.1	Protected Environment Isolated by Processor Features .....	29
D.2	Protected Environment in a separate ASIC.....	29

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# 1 SCOPE

This specification defines a reference architecture for the implementation of Trusted Computing technologies in modern mobile platforms. The cornerstone of this architecture is a Protected Environment that provides isolated resources for Trusted Applications. This architecture allows any possible implementation of a Protected Environment that meets the security requirements defined in this specification. Several example implementations are described in Annex D. The examples in this document are not mandatory.

The target audience of this specification includes designers, developers, and implementers of trusted computing technologies in mobile platforms.

This specification supports the use cases defined in [1].

## 1.1 Key Words

The key words “MUST,” “MUST NOT,” “REQUIRED,” “SHALL,” “SHALL NOT,” “SHOULD,” “SHOULD NOT,” “RECOMMENDED,” “MAY,” and “OPTIONAL” in this document normative statements are to be interpreted as described in RFC-2119, Key words for use in RFCs to Indicate Requirement Levels.

## 1.2 Statement Type

Please note a very important distinction between different sections of text throughout this document. There are two distinctive kinds of text: informative comment and normative statements. Because most of the text in this specification will be of the kind normative statements, the authors have implicitly defined it as the default and, as such, have specifically called out text of the kind informative comment. They have done this by flagging the beginning and end of each informative comment and highlighting its text in gray. This means that unless text is specifically marked as of the kind informative comment, it can be considered a kind of normative statements.

### EXAMPLE: Start of informative comment

This is the first paragraph of 1–n paragraphs comprising text of the kind *informative comment* ...

This is the second paragraph of text of the kind *informative comment* ...

This is the nth paragraph of text of the kind *informative comment* ...

To understand the TCG specification the user must read the specification. (This use of MUST does not require any action).

### End of informative comment

In some cases, entire sections are comprised of informative text, in order to provide background information or context for the normative statements in other sections of this specification. These sections are labelled by appending “INFORMATIVE” to the section title.

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## 1.4 Document Structure

This document is divided and organized into the following Sections:

- Section 2 contains a reference glossary defining the terms and definitions used throughout this specification.
- Section 3 is a brief introduction to the motivation and core concepts of this specification.
- Section 4 provides an introduction to Roots of Trust.
- Section 5 describes the boot sequences used by various mobile platforms.
- Section 6 defines requirements for a Mobile Platform that supports a Protected Environment.
- Section 7 defines requirements for a Protected Environment to host a Trusted Application.
- Section 8 defines requirements for a Trusted Application implementation to operate securely within a Protected Environment.
- Annex A provides an informative overview of typical mobile platform structures.
- Annex B gives example implementations of the boot sequences used by various mobile platforms.
- Annex C introduces Trusted Computing interface APIs that support this Mobile Reference Architecture.
- Annex D describes examples of implementations of Protected Environments.

## 2 Glossary

GLOSSARY TERM	DESCRIPTION
Application-specific integrated circuit (ASIC)	A microchip that is designed for a specific application (in contrast to a general-purpose chip such as a microprocessor).
Attestation	The process of vouching for the accuracy of information. A platform can attest to its description of platform characteristics that affect the integrity (trustworthiness) of the platform. Attestation requires reliable evidence of the attesting entity (modified from [2]).
Device	A shorthand in this document for a Mobile Platform.
DICE	The Device Identifier Composition Engine (DICE) is an immutable, or securely updated, engine that executes very early in the boot cycle and derives a value from a Unique Device Secret (UDS) and the cryptographic identity of the first mutable code on the platform [3].
Fuse	A mechanism of internal switches within the ASIC that can be electrically blown to enable write-once non-volatile storage of information within that ASIC.
Integrity Measurement	A value representing a platform characteristic that affects the integrity of a platform [2].
Measured Boot	A boot process where images are measured (for example by calculating their digests) and the measurements are extended into integrity-protected storage. See Annex B for more details.
Mobile Platform	A physical entity encompassing all hardware, firmware, software, and data necessary for it to function and provide services to an end user. Also known as a Mobile Device, or just Device in this specification. A laptop, tablet, smartphone, feature phone, basic phone, or other similar device that typically includes cellular connectivity (2G, 3G, LTE, 5G, etc.) and/or Internet connectivity (Ethernet, WiFi, VoMax, etc.) and associated protocol stacks.
Protected Environment	A functional element with its own execution and memory resources that are isolated from other components. See Sections 6 and 7 for details.
Rich Operating System (Rich OS)	An environment optimized for versatility and features where the bulk of device applications are executed. This environment is also called the Rich Execution Environment, Regular Execution Environment, or REE.
Root of Trust (RoT)	Roots of Trust (RoT) provide the foundation of any Trusted Computing architecture. They are hardware, firmware, and software components that perform specific, critical security functions [2], [4], [5], [6], [7].
Root of Trust for Confidentiality (RTC)	A Root of Trust providing confidentiality for secrets stored in secure storage locations (modified from [2]).
Root of Trust for Integrity (RTI)	A Root of Trust providing integrity for data stored in secure storage locations (modified from [2]).
Root of Trust for Measurement (RTM)	A Root of Trust that makes the initial integrity measurement, and adds it to a tamper-resistant storage location (modified from [2]).
Root of Trust for Reporting (RTR)	A Root of Trust that provides authenticity and non-repudiation services for the purposes of attesting to the origin and integrity of platform characteristics [2].
Root of Trust for Storage (RTS)	The combination of the RTC and RTI [2].
Root of Trust for Update (RTU)	A Root of Trust for Verification that verifies the integrity and authenticity of an update payload before initiating the update process [2].
Root of Trust for Verification (RTV)	A Root of Trust that verifies an integrity measurement against a policy or expected value [modified from 2].

Secure Boot	A boot process where images are validated before execution (for example by calculating their digests and comparing them to expected values). See Annex B for more details.
Trusted Platform Module (TPM)	An implementation of the functions defined in the TCG Trusted Platform Module Specification; the set of Roots of Trust with Shielded Locations and Protected Capabilities [2].
TPM Mobile	An implementation that conforms to the TCG TPM 2.0 Mobile Common Profile [8].
Trusted Application (TA)	An application that runs as an isolated process within a Protected Environment.
Unified Extensible Firmware Interface (UEFI)	A software interface between an operating system and platform firmware. See [9] for details.

**DRAFT**

### 3 Introduction

#### Start of informative comment

The evolution of mobile computing has led to smart devices that are in almost constant personal and business use. Users routinely access email, social media, application stores, financial institutions, and a variety of other services from their mobile platforms. Manufacturers, system integrators, and developers of secure applications can incorporate Trusted Computing mechanisms to provide security for and trust in these mobile use cases. Trusted Computing mechanisms include techniques to ensure platform integrity, resource isolation, and protected storage.

This specification adapts Trusted Computing concepts to apply them to mobile platforms. The diversity of the mobile market place and underlying mobile hardware structures (see Annex A) require a sound security model on which to build trusted services. That diversity also subjects mobile platforms to significant supply chain risk. This specification does not address mobile supply chain risk management directly, but many organizations, such as NIST, have provided guidance on the topic [10].

Central to this architecture is a Protected Environment. This specification defines a set of requirements for a Protected Environment to provide sufficient isolation of memory and execution resources for Trusted Applications. This specification places no limitations on how a Protected Environment is implemented, as long as it meets the requirements. Some examples of Protected Environment implementations are:

- Environments isolated by processor features [11][12][13]
- A virtual environment hosted by a hypervisor
- A physically isolated Application-Specific Integrated Circuit (ASIC) [14]

To improve the understanding of the architecture, example implementation models of a Protected Environment are described in informative Annex D.

#### End of Informative Comment

DRAFT



## 4 Roots of Trust

### Start of informative comment

Roots of Trust (RoT) are the foundation of any Trusted Computing architecture. The RoT are a prerequisite for implementing a Protected Environment capable of hosting Trusted Applications. The RoT can be implemented as part of a Protected Environment, as dedicated hardware, or as other components, depending on the architecture of a mobile platform. A detailed discussion of RoT is beyond the scope of this specification, but several organizations have defined RoT, including the TCG [2], [3], NIST [4], GlobalPlatform [5], and ARM [6].

An RoT is a component that enables discovery of a platform's trustworthiness. These are the primary RoT recognized in all public standards:

- Root of Trust for Measurement (RTM): An RoT that makes the initial integrity measurement and records it in a tamper-resistant storage location.
- Root of Trust for Reporting (RTR): An RoT that provides authenticity and non-repudiation services for the purposes of attesting to the origin and integrity of platform characteristics.
- Root of Trust for Storage (RTS): An RoT providing confidentiality and integrity for data stored in secure storage locations.

The RTS can be decomposed, notionally or functionally, by the properties of confidentiality and integrity that it provides. In those cases, the following RoT definitions apply:

- Root of Trust for Confidentiality (RTC): An RoT providing confidentiality for secrets stored in secure storage locations.
- Root of Trust for Integrity (RTI): An RoT providing integrity for data stored in secure storage locations.
- Root of Trust for Storage (RTS): The combination of the RTC and RTI.

Many standards bodies have expanded the definition of RoT to encompass components that perform one or more security-specific functions that are critical to the enforcement of trusted platforms [4]. In order to maintain consistency with the standards in the mobile ecosystem, these RoT are included in the set of RoT of this specification:

- Root of Trust for Verification (RTV): An RoT that verifies an integrity measurement against a policy or expected value.
- Root of Trust for Update (RTU): An RoT that verifies the integrity and authenticity of an update payload before initiating the update process.

Finally, in some specifications, an RoT is trusted to behave in the expected manner because its misbehaviour cannot be detected [2], [4]. However, in typical mobile platform architectures, many low-level components that provide RoT functionality are verifiable during Secure Boot. In this specification, verifiable components that provide security capabilities for the platform are referred to as an RoT, despite the fact that their misbehaviour can be detected.

### 4.1 Chains of Trust

A trustworthy platform can establish a chain of trust from an RoT to other components, either executables or data, on the platform. Through this process, the RoTs establish the trustworthiness of a component. Trust in that component is then used to establish the trustworthiness of the next component in the chain.

Typically, a chain of trust extends a security function, or service, from an RoT to other components. Examples common to mobile platforms include the following:

- A Chain of Trust for Verification extends the verification service from the RTV to other components. The RTV verifies a component that includes a verification service similar to the RTV. Afterwards, the verified component can verify other components on the host mobile platform.
- A Chain of Trust for Confidentiality expands confidentiality protections to storage external to the secure storage locations. The RTC can protect keys that encrypt data stored in flash memory of a mobile platform.



- A Chain of Trust for Integrity extends integrity protections from the RTI to other components. For example, the RTI can include a key or other data that is used by the RTV to validate a component. This validation adds the component to a Chain of Trust for Integrity. Thereafter, modules that are validated based on data protected by the RTI or components of its Chain of Trust for Integrity are added to the Chain of Trust for Integrity themselves.

**End of Informative Comment**

**DRAFT**

## 5 Mobile Platform Boot Sequences

### Start of informative comment

This section describes the various stages that can comprise the boot sequence and establish a Protected Environment, with particular emphasis on typical mobile platform architectures.

### 5.1 Boot ROM

Once the hardware is initialized, it starts to execute the Boot ROM. The Boot ROM is software stored in ROM that is built into the ASIC. Boot ROM integrity is inherent because the cost of modifying on-chip ROM falls outside the bounds of most commercial threat models. The software in the Boot ROM performs further hardware initialization before initiating Secure Boot.

The Boot ROM serves as the RTV and forms the root of the Chain of Trust for Verification. The software in the Boot ROM locates the first boot image, typically in some form of non-volatile storage. The Boot ROM then measures the located image and verifies the measurement. The expected value of the measurement is based on keys and other data that are stored or derived from values in write-once memory. These values are either part of the RTI or a Chain of Trust for Integrity. If all checks pass, the platform executes the loaded software.

### 5.2 Secure Boot

The most widely implemented boot mechanism on mobile platforms is Secure Boot. In Secure Boot, each module of code acts as a link in both a transitive Chain of Trust for Verification tracing back to the RTV and a transitive Chain of Trust for Integrity tracing back to the RTI.

During Secure Boot, any module of code (or a data file in some cases) is measured and validated before it is loaded. The validity of the measurement is determined based on information stored either in write-once memory or in previously verified code modules. If any of these measurements fail validation, the software enters a remediation mode.

### 5.3 Measured Boot

Measured Boot is the process followed once an integrity-protected storage mechanism is available for recording new data during the boot phase. During Measured Boot, any module of code (or a data file in some cases) is measured before it is loaded. The measurement is recorded in integrity-protected storage and is available for use in binding or attestation schemes thereafter. Examples of suitable storage mechanisms include a TPM Mobile, a Device Identifier Composition Engine (DICE) [4], and a Secure Element (SE) [15]. The code module is then executed or the data is used, regardless of the value of the measurement.

### 5.4 Overlapping Secure Boot and Measured Boot

The Secure Boot and Measured Boot Phases can overlap during the boot sequence. As soon as an integrity-protected storage mechanism is available for recording new data, the measurement can be recorded. However, the measurements may still be validated by the RTV or a module on the Chain of Trust for Verification in order to proceed with the boot sequence.

There are potential benefits to overlapping Secure Boot and Measured Boot in this way. Secure Boot enforces control over the platform runtime environment and can implant a cryptographic key on the platform. Practically, that key will be unique to the platform, and not updated with platform software updates. Measured Boot provides measurement and attestation of the platform software over the course of software updates and uses the cryptographic key to attest to the measurements. This approach allows external entities to establish trust in the platform without the need to change platform keys with every software update.

### End of Informative Comment

## 6 Mobile Platform Requirements

### Start of Informative Comment

This section describes the requirements for a mobile platform to support a Protected Environment. A Protected Environment provides execution and memory resources that are isolated from other components of a platform. The isolated execution property of a Protected Environment distinguishes a Protected Environment from the Regular Operating System (OS) environment. A mobile platform can satisfy these requirements using any mechanisms. Annex A describes examples of high-level structural components of contemporary mobile platforms.

### End of Informative Comment

### 6.1 Mobile Platform Fundamental Requirements

- MF1. A mobile platform SHALL implement a Protected Environment using mechanisms that enable each Protected Environment to satisfy the requirements in Section 7.
- MF2. A mobile platform SHALL implement every cryptographic algorithm of this specification with a cryptographic strength [17] of at least 112 bits [18].
- MF3. A mobile platform SHALL provide a Protected Environment with access to an entropy source that complies with IETF RFC 4086 [19] and ISO/IEC 18031:2011 [20].
- MF4. A mobile platform SHALL provide a Protected Environment with a trusted source of time-of-day information [21] [22].

### Start of Informative Comment

Physical protections (such as the use of Read-Only Memory (ROM), fuses, physical separation, or some processor isolation mechanisms) are adequate substitutes for cryptographic protections as long as the physical protections are sufficient to defeat general-purpose attacks and at least simple hardware attacks, comparable to [23].

### End of Informative Comment

### 6.2 Mobile Platform Secure Boot Requirements

- MB1. A mobile platform SHALL implement the Boot ROM phase.
- MB2. A mobile platform SHALL implement the Secure Boot phase.
- MB3. A mobile platform SHALL cryptographically verify the integrity of all Protected Environment capabilities and extend a Chain of Trust for Integrity from the hardware Roots of Trust.

### 6.3 Mobile Platform Measured Boot Requirements

- MM1. A mobile platform SHALL implement the Measured Boot phase.
- MM2. A mobile platform SHALL generate evidence that unambiguously identifies every platform firmware and software component that implements a Protected Environment and is used by a Protected Environment [24].
- MM3. A mobile platform SHALL record evidence that unambiguously identifies every platform firmware and software component that implements a Protected Environment and is used by a Protected Environment, in integrity-protected storage [24].

MM4. A mobile platform SHALL provide evidence in requirements MM2 and MM3 that is sufficient to identify the manufacturer and the version of each component.

## 6.4 Mobile Platform Update Requirements

MU1. A mobile platform SHALL provide a means to update a Protected Environment or any software on which a Protected Environment depends.

MU2. A mobile platform SHALL cryptographically verify the integrity and authenticity of updates to a Protected Environment or any software on which a Protected Environment depends, before the updates are installed.

MU3. A mobile platform SHALL prevent rollback of software implementing a Protected Environment to older versions.

## 6.5 Mobile Platform Runtime Requirements

MR1. A mobile platform SHALL isolate a Protected Environment from all other execution environments.

MR2. A mobile platform SHALL protect communications between a Protected Environment and all other execution environments.

MR3. A mobile platform SHALL implement communication mechanisms between a Protected Environment and other execution environments, based on published standards.

## 6.6 Mobile Platform Storage Requirements

MS1. A mobile platform SHALL provide a Protected Environment with exclusive access to non-volatile storage locations.

MS2. A mobile platform SHALL prevent the disclosure of Protected Environment information stored either in run time memory or in non-volatile storage locations, through debug facilities of a mobile platform.

MS3. A mobile platform SHALL isolate the execution resources of cryptographic services or capabilities used by a Protected Environment from entities external to that Protected Environment.



















## C. Trusted Computing Communication Interfaces

### Start of Informative Comment

The TCG has developed mobile specifications that are compatible with specifications published by Standards Development Organizations (SDOs) within the mobile ecosystem. Among these, there are several communication and programming interfaces that support the requirements of Protected Environment and Trusted Application implementations. The following is a list of example interfaces, but any standardized interfaces that meet the requirements of this specification are candidates for this Mobile Reference Architecture. For more details about the interfaces on this list, see the referenced specifications.

- The TCG TPM 2.0 Trusted Software Stack contains multiple layers of application- and system-level interfaces to isolate TPM 2.0 client applications from low-level details of interfacing to the TPM. Application developers can use this software specification to develop interoperable client applications [30].
- The TCG TPM 2.0 Mobile Command Response Buffer (CRB) Interface is a kernel interface to a TPM that is intended to work with all architectures. The CRB Interface makes it possible to write a driver that can interact with a TPM, whether the TPM is a discrete component on a peripheral bus or a Trusted Application in a Protected Environment [31].
- The GlobalPlatform TEE Internal Core API [32] is a set of C APIs exposed to Trusted Applications running in the TEE. The APIs provide a variety of functions, such as memory management, communications with client applications in the Regular Execution Environment (REE), trusted storage, cryptographic facilities, time management, and peripheral interfaces.
- The GlobalPlatform TEE Client API [33] defines communications between applications running in the REE and applications running in the TEE. This API serves as a base layer on which developers can implement higher level protocols.
- The GlobalPlatform TEE Sockets API is a generic C interface that allows Trusted Applications in a TEE to establish socket-style network communications with a remote server [34]. The TEE Sockets API specification does not require particular protocols or data structures, but it does include guidance on implementing some protocols, such as TCP and UDP.
- The Internet Engineering Task Force (IETF) TEE Provisioning (TEEP) Working Group is developing TEEP Architecture [35] and TEEP Protocol [36] specifications in order to provide TEEs with lifecycle management and security domain management of Trusted Applications.
- The GlobalPlatform TEE Management Framework describes a security model for the administration of TEEs, TAs, and their corresponding Security Domains (SDs). The specification addresses the various roles and responsibilities of multiple stakeholders involved in the administration of TEEs and TAs, as well as the mechanisms of administration [37].
- The GlobalPlatform TEE Secure Element API [15] is a thin interface to support communication to an SE connected to the device that implements a TEE. This API defines a transport interface based on the Open Mobile API Specification [38]. The TEE SE API is particularly pertinent to those implementing use cases involving both a TEE and an SE.
- The GlobalPlatform Trusted Platform Services (TPS) Client API [39] is a draft specification that enables access to platform services offered by standardized secure components. GlobalPlatform and Trusted Computing Group members are collaborating to keep specifications compatible, such that the TPS Client API can be used to access a SE, a TEE, or a TPM.

### End of Informative Comment

## D. Protected Environment Implementation Examples

### Start of Informative Comment

### D.1 Protected Environment Isolated by Processor Features

In this example, a Protected Environment and the Rich OS share the same processor(s). The separation of a Protected Environment from the Rich OS is achieved by a processor mode or extension [11], [12], [13] similar to the mobile platform structure described in Annex A.1. Typically, a trusted operating system runs within a Protected Environment and hosts Trusted Applications.

During the boot sequence, the system initializes a Protected Environment first, so that the Protected Environment can complete its boot sequence without interference from code executing in the Rich OS. The Protected Environment also reserves the resources it requires, leaving the remainder for use by the Rich OS. When Protected Environment initialization is completed, the Rich OS boot process starts.

An example implementation of this Protected Environment mode is a GlobalPlatform TEE [40]. A GlobalPlatform compliant TEE that includes anti-rollback features should be capable of meeting all Protected Environment requirements.

### D.2 Protected Environment in a separate ASIC

In this model, a Protected Environment is not located in the same ASIC as the application processors. This example is similar to the mobile platform structure described in Annex A.3, in particular Model 3 of Figure 3. A communications link is necessary between the Applications ASIC and a Protected Environment ASIC.

This model involves a different boot process because the two ASICs are independent. If a Protected Environment ASIC does not support a Rich OS, a single boot sequence is sufficient. For maximum security, in this case, a Protected Environment ASIC could boot from internal non-volatile memory. The Application ASIC follows a standard boot sequence as defined in Section 5. The Application ASIC can connect to a Protected Environment ASIC after a Protected Environment is established, in order to store measurements securely during Measured Boot.

An SE is an example of an external ASIC that could host a Protected Environment. SEs are highly secure ASICs designed to protect high value assets (such as financial secrets in credit cards or access secrets for Mobile Networks) in mobile platforms, from both hardware and software attacks. The interfaces and security mechanisms of SEs are defined by a number of ISO [41], ETSI [42] and GlobalPlatform [43] standards. Common Criteria certification [44] and compliance testing [45] are required for commercial products.

There is a set of GlobalPlatform standards [46] that define an execution environment within an SE that supports programs written in a subset of Java. SEs implementing these standards are known as JavaCards. The capabilities and protection level offered by JavaCards exceed the requirements for a Protected Environment, although the amount of secure storage provided is limited.

### End of Informative Comment