Errata Version 1.0
April 6, 2017

For

TCG TPM I2C Interface Specification

Family “2.0”
Level 00 Revision 1.00
August 05, 2016

Contact: admin@trustedcomputinggroup.org
Disclaimers, Notices, and License Terms

THIS ERRATA IS PROVIDED "AS IS" WITH NO WARRANTIES WHATSOEVER, INCLUDING ANY WARRANTY OF MERCHANTABILITY, NONINFRINGEMENT, FITNESS FOR ANY PARTICULAR PURPOSE, OR ANY WARRANTY OTHERWISE ARISING OUT OF ANY PROPOSAL, SPECIFICATION OR SAMPLE.

Without limitation, TCG and its members and licensors disclaim all liability, including liability for infringement of any proprietary rights, relating to use of information in this specification and to the implementation of this specification, and TCG disclaims all liability for cost of procurement of substitute goods or services, lost profits, loss of use, loss of data or any incidental, consequential, direct, indirect, or special damages, whether under contract, tort, warranty or otherwise, arising in any way out of use or reliance upon this specification or any information herein.

This document is copyrighted by Trusted Computing Group (TCG), and no license, express or implied, is granted herein other than as follows: You may not copy or reproduce the document or distribute it to others without written permission from TCG, except that you may freely do so for the purposes of (a) examining or implementing TCG specifications or (b) developing, testing, or promoting information technology standards and best practices, so long as you distribute the document with these disclaimers, notices, and license terms.

Contact the Trusted Computing Group at www.trustedcomputinggroup.org for information on specification licensing through membership agreements.

Any marks and brands contained herein are the property of their respective owners.
Table of Contents

1. Introduction .................................................................................................................. 4
2. Clarifications .................................................................................................................. 5
   2.1 Clarification 1 ......................................................................................................... 5
   2.2 Clarification 2 ......................................................................................................... 5
3. Errata .......................................................................................................................... 6
   3.1 Errata 1 ................................................................................................................... 6
1. Introduction

This document describes errata and clarifications for the TCG TPM I2C Interface Specification Level 00 Revision 1.00 as published. The information in this document is likely – but not certain – to be incorporated into a future version of the specification. Suggested fixes proposed in this document may be modified before being published in a later TCG Specification. Therefore, the contents of this document are not normative and only become normative when included in an updated version of the published specification. Note that since the errata in this document are non-normative, the patent licensing rights granted by Section 16.4 of the Bylaws do not apply.
2. Clarifications

2.1 Clarification 1

Section 6.5 TPM_HASH_START Table 2 I2C TPM Register overview specifies the TPM_HASH_START register as follows:

| 0x28 | TPM_HASH_START | 1 | This signals the start of the hash operation. Only available when locality 4 is selected | Write only |

It might happen that the phrase “when locality 4 is selected” is misinterpreted as “Locality 4 is the active Locality”. Therefore the following clarification will be added in a future version of the specification:

There are two possibilities to access TPM_HASH_START:

1) Write 0x04 to TPM_LOC_SEL followed by a write to TPM_HASH_START, which automatically sets TPM_ACCESS.activeLocality for Locality 4.

2) Write 0x04 to TPM_LOC_SEL followed by a write to TPM_ACCESS.requestUse, then write to TPM_HASH_START.

2.2 Clarification 2

Section 6.5.1 TPM_LOC_SEL and section 6.6 Interface Locality Usage per Register (Table 11) have no restrictions when TPM_LOC_SEL may be accessed. TPM_LOC_SEL should not change its value during a DRTM execution sequence. Therefore this clarification recommends I2C TPM implementations should ignore any write to TPM_LOC_SEL during a DRTM execution sequence.
3. Errata

3.1 Errata 1

Section 6.4 Table 1 contains in the 2nd column the values for the Locality Selection Register. Those values are in hexadecimal representation but contain incorrect numbers for the Locality range “Reserved for vendor use” and for the range “32 – 255” The correct values can be seen in the corrected table below (corrected values are highlighted in yellow color).

<table>
<thead>
<tr>
<th>Locality Priority</th>
<th>Locality Selection Register value</th>
<th>Locality</th>
<th>Value of locality modifier (see the PTP Error! Reference source not found.)</th>
<th>Mandatory (M) Optional (O)</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>0x00</td>
<td>0</td>
<td>0000 0001b</td>
<td>M</td>
</tr>
<tr>
<td>4</td>
<td>0x01</td>
<td>1</td>
<td>0000 0010b</td>
<td>O</td>
</tr>
<tr>
<td>3</td>
<td>0x02</td>
<td>2</td>
<td>0000 0100b</td>
<td>O</td>
</tr>
<tr>
<td>2</td>
<td>0x03</td>
<td>3</td>
<td>0000 1000b</td>
<td>O</td>
</tr>
<tr>
<td>1</td>
<td>0x04</td>
<td>4</td>
<td>0001 0000b</td>
<td>O</td>
</tr>
<tr>
<td>6</td>
<td>0x05 – 0x1F</td>
<td>Reserved for vendor use</td>
<td>n.a.</td>
<td>n.a.</td>
</tr>
<tr>
<td></td>
<td>0x20 – 0xFF</td>
<td>32 – 255</td>
<td>0010 0000b – 1111 1111b</td>
<td>O</td>
</tr>
</tbody>
</table>